

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	((("6724648") or ("5781482"))).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/21 08:56
S2	5	("5297098" "5303190" "5644546" "5668770" "6307803").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 15:48
S3	2	("6724648").URPN.	USPAT	OR	ON	2005/09/30 15:48
S5	84	6T ADJ memory ADJ cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:01
S6	327	6T ADJ SRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 15:50
S7	13	("4901284" "5604705" "5936892" "5956279" "5963490" "6081464" "6088286" "6115312" "6240048" "6414890" "6507529" "6683805" "6724648").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 16:02
S8	11	mcclure-david.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 15:25
S9	11074	STMicroelectronics.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 15:24
S10	224	mcclure-david-c.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 15:25

S11	49	S10 AND S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 15:25
S12	2161	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 11:07
S13	256	307/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 10:20
S14	72	323/220.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 10:20
S15	111	365/188.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 11:07
S16	1131	365/190.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 11:08
S17	753	365/191.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 11:08
S18	605	tamper NEAR3 circuit\$3 AND memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 14:53

S19	1160	tamper NEAR3 detect\$3 AND memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 14:48
S20	245	S18 AND ("smart card")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 15:34
S21	148467	("memory cell") ("data latch")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 15:35
S22	126	S21 AND (true complement) ADJ node	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 15:53
S23	24	S22 AND ("pass gate")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 15:35
S24	23	S21 AND (true AND complement) ADJ node	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 15:40
S25	5	S24 AND ("pass gate")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/17 15:41
S26	44152	("memory cell" "data latch") AND short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:24

S27	13986	("memory cell" "data latch") SAME short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:05
S28	2162	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S29	256	307/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S30	72	323/220.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S31	111	365/188.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S32	1132	365/190.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S33	755	365/191.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:25
S34	629	S27 AND (S28 S29 S30 S31 S32 S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:30

S35	3721	data SAME eras\$3 SAME short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:32
S36	22	S35 AND (S28 S29 S30 S31 S32 S33)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:36
S37	39419	data NEAR3 short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:39
S39	277	data NEAR3 short\$3 SAME (destroy erase)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:42
S40	1	S27 SAME ((true complement) ADJ node)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:43
S41	152	S27 SAME destroy\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:44
S42	0	("6T" 6-T) SAME short\$3 SAME "reference voltage" SAME destroy\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:58
S43	87	short\$3 SAME "reference voltage" SAME destroy\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:59

S44	3	S43 AND ("6T" 6-T)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 09:59
S45	101	("6T" 6-T) SAME short\$3 SAME destroy\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:00
S46	700	("6T" 6-T) NEAR3 cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:02
S47	359	S46 AND short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:02
S48	152	("memory cell" "data latch") SAME short\$3 SAME destroy\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:07
S49	644	("memory cell" "data latch") SAME shorting	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 10:07
S50	2	("20020016914").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/18 15:40
S51	33	"true node" AND "complement node"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 15:40

S52	148554	("memory cell") ("data latch")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 15:53
S53	126	S52 AND (true complement) ADJ node	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/18 15:54
S54	888	(true AND complement) SAME short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 08:45
S55	888	(true AND complement) SAME short\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 08:46
S56	401	S55 AND voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:05
S57	176	(true AND complement) SAME short\$3 SAME voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:55
S58	43096	short\$3 SAME (destroy\$3 eras\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:56
S59	1152	365/210.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:57

S60	2833	365/189.05.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:57
S61	2162	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/19 10:59
S62	112	S58 AND (S59 S60 S61)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:17
S63	3017	"same" ADJ control ADJ signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:40
S64	2388	different ADJ control ADJ signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:22
S67	40	S63 SAME S64	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:23
S68	32513	"same" ADJ voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:40
S69	32389	different ADJ voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 14:40

S70	1179	S68 SAME S69	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:11
S78	24592	inverter SAME latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:24
S79	1152	365/210.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:25
S80	2840	365/189.05.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:25
S81	2162	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:25
S82	1450	S78 AND (S79 S80 S81)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:36
S83	19252	inverter WITH (PMOS p-channel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:37
S84	10881	inverter WITH (coupl\$3 connect\$3) WITH (PMOS p-channel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:38

S85	764	S84 AND (S79 S80 S81)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 15:38
S86	1152	365/210.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 09:02
S87	2840	365/189.05.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 09:02
S88	2162	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 09:02
S89	170	latch SAME ("reference voltage") AND (S86 S87 S88)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 10:38
S90	76	short\$3 SAME ("bit line" bit-line) SAME ("word line" word-line) SAME ("reference voltage")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 14:15
S91	64	("pull up" pull-up) SAME ("word line" word-line) SAME ("reference voltage")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 14:16
S92	6	("pull up" pull-up) SAME ("word line" word-line) SAME ("reference voltage") AND (S86 S87 S88)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 14:38